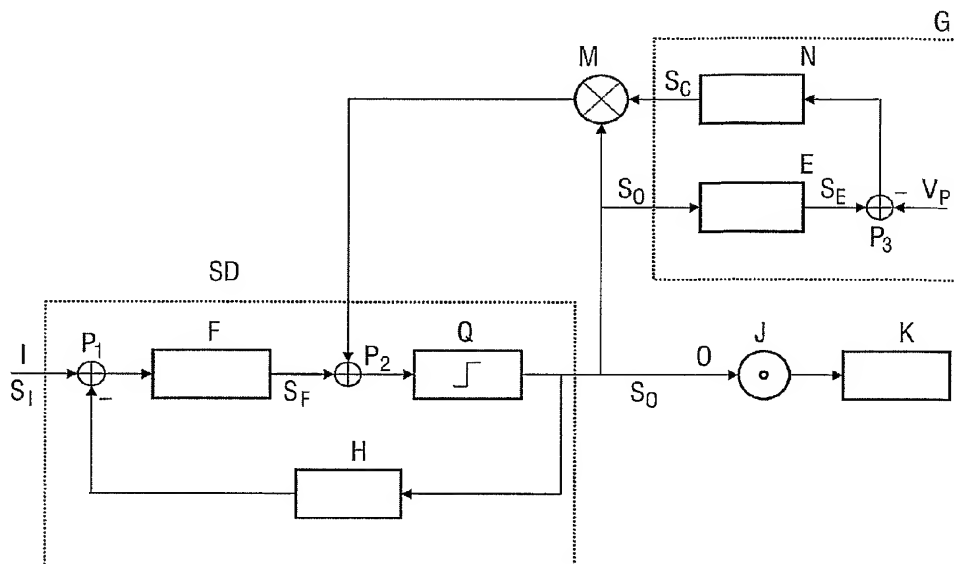




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(54) Title: METHOD OF REDUCING INTER-SYMBOL INTERFERENCE, A SIGMA-DELTA CONVERTER FOR PERFORMING THIS METHOD AND A STORAGE MEDIUM CONVEYING INFORMATION GENERATED BY THIS METHOD



(57) Abstract: A method and arrangement of reducing inter-symbol interference occurring at the digital to analog conversion of a one bit digital signal is provided. During the generation of the one-bit digital signal in a sigma-delta converter the edge-density of the digital signal is measured, the result of the measurement is multiplied with the digital signal and the result of the multiplication is added to the input of the quantizer generating the digital signal. The invention also covers storage media comprising a one-bit digital signal generated in accordance with the invention.



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